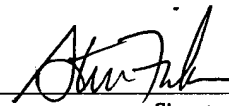


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TRANSMITTAL OF FORMAL DRAWINGS					Docket No. BUR920010131US1 (15054)	
In Re Application Of: Kenneth J. Goodnow, et al.						
Application No. 10/064,582	Filing Date July 29, 2002	Examiner Dipakkumar B. Gandhi	Customer No. 23389	Group Art Unit 2138	Confirmation No. 5111	
Invention: SYSTEM AND METHOD FOR CORRECTING TIMING SIGNALS INTEGRATED CIRCUITS						
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450						
Transmitted herewith are: 2 sheets of formal drawing(s) for this application. <input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).						
_____  <i>Signature</i> Steven Fischman Registration No. 34,594 SCULLY, SCOTT, MURPHY & PRESSER, P.C. 400 Garden City Plaza, Suite 300 Garden City, New York 11530 (516) 742-4343			Dated: June 2, 2006 <div style="border: 1px solid black; padding: 5px; margin-top: 10px;"><div style="text-align: center;">I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on</div><div style="text-align: center; margin-top: 10px;">_____ (Date)</div><div style="text-align: center; margin-top: 10px;">_____ Signature of Person Mailing Correspondence</div><div style="text-align: center; margin-top: 10px;">_____ Typed or Printed Name of Person Mailing Correspondence</div></div>			